

لیست مقالات گروه مهندسی برق-الکترونیک- کرایش مدارهای مجتمع الکترونیک:  
- مقالات پذیرفته شده برای چاپ یا چاپ شده:

- 1) A. Karimi, A. Rezai, "A Novel design for Memristor-based n to 1 multiplexer using new IMPLY logic approach", *IET Circuits, Devices & Systems*, vol. 13, no.5, pp. 647 – 655, 2019.
- 2) A. Karimi, A. Rezai, M. M. Hajhashemkhani, "Ultra-low power pulse-triggered CNTFET based Flip-Flop", *IEEE Transactions on Nanotechnology*, vol. 18, pp. 756 – 761, 2019.
- 3) M. Niknezhad Divshali, A. Rezai, S. Fallahieh Hamidpor, "Design of novel counter circuit in quantum dot cellular automata technology" *International Journal of Theoretical Physics*, vol. 58, no.8, pp. 2677–2691, 2019.
- 4) Z. Taheri, A. Rezai, "A novel fault-tolerant full adder for quantum-dot cellular automata based on new majority gate", *Accepted for publication in Facta Universitatis, Series: Electronics and Energetics*, 2019.
- 5) Z. Dadgar, A. Rezai, "An efficient design for a single layer full adder and a ripple carry adder in quantum-dot cellular technology" *Journal of Nano- and Electronic Physics*, vol.11, no.3, pp.03034-1 - 03034-4, 2019.
- 6) H. Roshany, A. Rezai, "Novel efficient design of multi-layer QCA RCA", *International Journal of Theoretical Physics*, vol.58, no. 6, pp.1745–1757, 2019.
- 7) Y. Adelnia, A. Rezai, "A novel adder circuit design in quantum-dot cellular automata technology" *International Journal of Theoretical Physics*, vol. 58, no. 1, pp. 184-200, 2019.
- 8) A Shiri, A Rezai, H Mahmoodian, "Design of efficient coplanar 1-bit comparator circuit in QCA technology", *Facta Universitatis, Series: Electronics and Energetics*, vol.32, no. 1, pp.119-128, 2019.
- 9) M. Niknezhad Divshali, A. Rezai, A. Karimi, "Towards multilayer QCA SISO shift register based on efficient D-FF circuits", *International Journal of Theoretical Physics*, vol. 57, no. 11, pp. 3326–3339, 2018.
- 10) I. Edrisi Arani, A. Rezai, "Novel Circuit Design of Serial-Parallel Multiplier In Quantum-dot Cellular Automata Technology", *Journal of Computational Electronics*, vol. 17, no. 4, pp.1771– 1779, 2018 .
- 11) A. Karimi, A. Rezai, "Novel design for Memristor-based full adder using new IMPLY logic approach", *Journal of Computational Electronics*, vol. 17, no. 3, pp. 1303-1314, 2018 ..
- 12) A. Karimi, A. Rezai, M. M. Hajhashemkhani, "A novel design for ultra-low power pulse-triggered D-Flip-Flop with optimized leakage power", *Integration, the VLSI journal* vol. 60, no. 1, pp. 160–166, 2018 .
- 13) M. Balali, A. Rezai, "Design of high-speed and low-complexity single-layer 4-bit ripple carry adder in QCA technology" *International Journal of Theoretical Physics*, vol. 57, no. 7, pp. 1948– 1960, 2018 .
- 14) D. Mokhtari, A. Rezai, H. Rashidi, F. Rabeie, S. Emadi, A. Karimi, "Design of novel efficient full adder architecture for quantum-dot cellular automata technology" *Facta Universitatis, Series: Electronics and Energetics*, vol. 31, no. 2, pp. 279-285, 2018 .
- 15) R. Mokhtarii, A. Rezai, "Investigation and design of novel comparator in quantum-dot cellular automata technology" *Journal of Nano- and Electronic Physics*, vol. 10, no. 5, pp. 05014-1 - 05014-4, 2018 .
- 16) M. Nazeri, A. Rezai, "A novel and fast hardware implementation for Golay code encoder", *Advances in Electrical and Electronic Engineering journal*, vol. 16, no. 4, pp. 521-527, 2018.

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- 19) M. Balali, A. Rezai, H. Balali, F. Rabeie, and S. Emadi, "Towards Coplanar quantum-dot cellular automata adders based on efficient three-input XOR gate" *Results in Physics*, vol. 7, pp. 1389–1395, 2017.
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- 21) S. Sadoni, and A. Rezai, "High-bandwidth buffer amplifier for liquid crystal display applications" *Facta Universitatis, Series: Electronics and Energetics*, vol. 30, no. 4, pp. 549-556, 2017.
- 22) H. Rashidi, A. Rezai, "Design of novel efficient multiplexer architecture for quantum-dot cellular automata" *Journal of Nano- and Electronic Physics*, vol. 9, no. 1, pp. 1-7, 2017.
- 23) A. Karimi, and A. Rezai, "Improved device performance in CNTFET using genetic algorithm" *ECS Journal of Solid State Science and Technology*, vol. 6, no.1, pp. M9-M12, 2017.
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- 27) S. Soltany, A. Rezai, "A novel low power and low voltage bulk-input four-quadrant analog multiplier in voltage mode", *International Journal of Multimedia and Ubiquitous Engineering*, vol. 11, no.1, pp. 159-168, 2016.

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- 30) M. Nazeri, A. Rezai, and H. Azis, "An efficient architecture for Golay code", *Accepted for publication in proc. IEEE 2<sup>nd</sup> East Indonesia Conference on Computer and Information Technology (EIConCIT)*, 2018.
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- 32) M. Abbasi, A. Rezai, A. Karimi, "A novel approach for improving the modular exponentiation performance" in proc. *IEEE International Conference on Advanced Information and Communication Technologies (IEEE AICT 2017)*, Ukraine, pp. 43-46, 2017.

- 33) M. Balali, A. Rezai, H. Balali, F. Rabeie, and S. Emadi, “A novel design of 5-input majority gate in quantum-dot cellular automata technology” in proc. *IEEE Symposium on Computer Applications & Industrial Electronics (ISCAIE 2017)*, Malaysia, pp. 13-18, 2017.
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- 36) S. Soltany, A. Rezai, “A New Low Power Four Quadrant Analog Multiplier” in proc. 2<sup>nd</sup> international Workshop on Information Technology and Computer Science, ASTL:106, pp. 33-36, 2015.
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